



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/164,388	09/30/1998	HON WAH CHIN	CISCPO54	6603

22434 7590 09/20/2006

BEYER WEAVER & THOMAS, LLP  
P.O. BOX 70250  
OAKLAND, CA 94612-0250

EXAMINER

PRIETO, BEATRIZ

ART UNIT PAPER NUMBER

2142

DATE MAILED: 09/20/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Supplemental  
Notice of Allowability**

Application No.

09/164,388

Examiner

Prieto Beatriz

Applicant(s)

CHIN, HON WAH

Art Unit

2142

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/21/05.
2. ☒ The allowed claim(s) is/are 1-43 and 45-54.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some\* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

**BEATRIZ PRIETO**  
**PRIMARY EXAMINER**

***Examiner's Amendment***

1. An Examiner's Amendment to the record appears below. Should the changes or additions be unacceptable to Applicant, an amendment may be filed as provided by 37 C.F.R. § 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the Issue Fee.

***Amendment to the Claims***

2. The following examiner's amendment is solely for correcting minor noted lack of antecedent basis in claims 49 and 50. Authorization for this examiner's amendment was given by Elise Heilbrunn (Reg. No. 42,649) on August 04, 2006 to correct minor noted informality.

**Please amend the claims as follows:**

1. (Previously Amended) A method for providing an inbound controller for a router, the router having an inbound port and an outbound port, a memory, and a CPU, the inbound controller being adapted for receiving an inbound packet at the inbound port, the method comprising:
  - providing a plurality of inbound queues for the inbound port;
  - receiving an inbound packet at the inbound port;
  - classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria;
  - storing the inbound packet in the selected- one of the plurality of inbound queues; and
  - determining when one of the plurality of inbound queues storing a plurality of packets is ready to be moved to an entry in an outbound. queue associated with the outbound port, the outbound queue being capable of storing a reference to a multiplicity of inbound queues such that a reference to each of the multiplicity of inbound queues is separately stored in a different one of a plurality of entries in the outbound queue, each of the multiplicity of inbound queues storing a. plurality of packets to be separately transmitted.
2. (Previously Amended) The method as recited in claim 1, further including:
  - asserting an interrupt when it is determined that one of the plurality of inbound queues is ready to be moved to an outbound queue.
3. (Original) The method as recited in claim 1, wherein classifying the inbound packet includes:
  - selecting inbound packet sorting criteria;
  - obtaining, packet sorting data for the inbound packet, the packet sorting data being associated with the packet sorting criteria; and
  - sorting the inbound packet into one of the plurality of inbound queues according to the packet sorting data.
4. (Previously Amended) The method as recited in claim 1, the selected one of the plurality of inbound queues corresponding to one of a plurality of outbound queues, the method further comprising:
  - transferring the selected one of the plurality of inbound queues storing a plurality of packets to the outbound queue associated with the outbound port such that a reference to the selected inbound queue storing a plurality of packets is stored in a single one of a plurality of entries in the outbound queue.
5. (Original) The method as recited in claim 1, wherein storing the inbound packet includes:
  - obtaining an available packet buffer from a free pool of available packet buffers;

placing the inbound packet in the packet buffer; and  
storing the packet buffer in the inbound queue.

6. (Original) The method as recited in claim 1, wherein determining when one of the plurality of inbound queues is ready to be moved to an outbound queue includes:

determining whether a number of packets in one of the plurality of inbound queues exceeds a maximum number of packets.

7. (Original) The method as recited in claim 1, wherein determining when one of the plurality of inbound queues is ready to be moved to an outbound queue includes:

determining whether a number of bytes in one of the plurality of inbound queues exceeds a maximum number of bytes.

8. (Original) The method as recited in claim 1, wherein determining when one of the plurality of inbound queues is ready to be moved to an outbound queue further includes:

determining whether a free pool of available memory has been depleted.

9. (Original) The method as recited in claim 1, wherein determining when one of the plurality of inbound queues is ready to be moved to an outbound queue further includes:

determining whether a maximum time limit has been exceeded.

10. (Previously Amended) A method for providing an outbound controller for a router, the router having an inbound port and an outbound port, a memory, and a CPU, the outbound controller being adapted for forwarding packets at the outbound port, the method comprising:

providing an outbound queue associated with the outbound port and being capable of storing a plurality of inbound queues;

receiving a notification to handle an inbound queue, the inbound queue storing a plurality of packets that are to be separately transmitted;

transferring the inbound queue storing a plurality of packets to a single entry in the outbound queue associated with the outbound port such that a reference to the inbound queue storing a plurality of packets is stored in one of a plurality of entries in the outbound queue; and

repeating the receiving and transferring steps for the plurality of inbound queues such that a reference to each of the plurality of inbound queues is separately stored in a different one of the plurality of entries in the outbound queue.

11. (Original) The method as recited in claim 10, wherein receiving the notification includes:  
receiving a notification from the CPU to handle the inbound queue.
12. (Original) The method as recited in claim 10, further including:  
transmitting packets stored in the outbound queue.
13. (Original) The method as recited in claim 10, wherein transmitting packets includes:  
selectively discarding packets stored in the outbound queue.
14. (Original) The method as recited in claim 10, wherein transmitting packets stored in the outbound queue further includes:  
obtaining at next one of the plurality of inbound queues stored in the outbound queue;  
transmitting selected packets stored in the next one of the plurality of inbound queues; and  
releasing memory associated with the next one of the plurality of inbound queues.
15. (Original) The method as recited in claim 14, wherein releasing the memory includes:  
storing the released memory in a free pool of available packet buffers.
16. (Original) The method as recited in claim 14, wherein releasing the memory includes:  
forming a new inbound queue to be used by an inbound controller.
17. (Original) The method as recited in claim 14, wherein releasing the memory includes:  
forming a queue to be used by the outbound controller during bi-directional operation.
18. (Previously Amended) The method as recited in claim 10, wherein transferring the inbound queue to the outbound queue further includes:  
ascertaining a priority of the inbound queue; and  
transferring the inbound queue to a single entry in the outbound queue according to the

priority of the inbound queue.

19. (Previously Amended) A method for forwarding a packet in a router, the router having a plurality of inbound ports and a plurality of outbound ports, a memory, and a CPU, the method comprising:

providing a plurality of inbound queues for one of the plurality of inbound ports;

providing a plurality of outbound queues, each one of the plurality of outbound queues corresponding to one of the plurality of outbound ports and being capable of storing a plurality of inbound queues;

receiving an inbound packet at the one of the plurality of inbound ports;

classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria;

storing the inbound packet in the selected one of the plurality of inbound queues;

repeating the steps of receiving, classifying, and storing until an interrupt is asserted; and

transferring one of the plurality of inbound queues storing a plurality of packets to one of the plurality of outbound queues corresponding to the packet sorting criteria when the interrupt is asserted such that a reference to the one of the plurality of inbound queues storing a plurality of packets is stored in a single one of a plurality of entries in the one of the plurality of outbound queues, wherein each of the plurality of packets in the one of the plurality of inbound queues is to be separately transmitted.

20. (Previously Amended) An inbound controller for a router, the router having an inbound port and an outbound port, a memory, and a CPU, the inbound controller being adapted for receiving an inbound packet at the inbound port, comprising:

a packet receiving module coupled to the inbound port, the packet receiving module being adapted for receiving an inbound packet;

wherein the memory has stored therein:

a plurality of inbound queues for the inbound port;

a classifier adapted for classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria;

a packet storing module coupled to the classifier, the packet storing module being adapted for storing the inbound packet in the selected one of the plurality of inbound queues; and

a module adapted for determining when one of the plurality of inbound queues is ready to be moved to an entry in an outbound queue associated with the outbound port, the outbound queue being capable of storing a multiplicity of inbound queues, a reference to each of the multiplicity

of inbound queues being stored in a different one of a plurality of entries in the outbound queue, each of the multiplicity of inbound queues storing a plurality of packets that are to be separately transmitted.

21. (Original) The inbound controller as recited in claim 20, further including:  
a module adapted for providing the determined one of the plurality of inbound queues.
22. (Original) The inbound controller as recited in claim 20, further including:  
a module adapted for asserting an interrupt when it is determined that one of the plurality of inbound queues is ready to be moved by the CPU to the outbound queue.
23. (Original) The inbound controller as recited in claim 20, wherein the packet storing module includes:  
a memory obtaining module adapted for obtaining an available packet buffer from a free pool of available packet buffers;  
a module adapted for placing the inbound packet in the packet buffer; and  
a module adapted for storing the packet buffer in the inbound queue.
24. (Original) The inbound controller as recited in claim 20, wherein the module adapted for determining when one of the plurality of inbound queues is ready to be moved to an outbound queue includes:  
a module adapted for determining whether a number of packets in one of the plurality of inbound queues exceeds a maximum number of packets.
25. (Original) The inbound controller as recited in claim 20, wherein the module adapted for determining when one of the plurality of inbound queues is ready to be moved to an outbound queue includes:  
a module; adapted for determining whether a number of bytes in one of the plurality of inbound queues exceeds a maximum number of bytes.
26. (Original) The inbound controller as recited in claim 20, wherein the module adapted for determining when one of the plurality of inbound queues is ready to be moved to an outbound queue includes:  
a module adapted for determining whether a free pool of available memory has been depleted.



27. (Original) The inbound controller as recited in claim 20, wherein the module adapted for determining when one of the plurality of inbound queues is ready to be moved to an outbound queue: includes:

a module adapted for determining whether a maximum time limit has been exceeded.

28. (Previously Amended) An outbound controller for a router, the router having an inbound port and an outbound port, a memory, and a CPU, the outbound controller being adapted for forwarding packets at the outbound port, comprising:

a module adapted for receiving a notification to handle an inbound queue associated with the inbound port, the inbound queue storing a plurality of packets;

wherein at least one of the CPU and the memory are adapted for storing an outbound queue associated with the outbound port, the outbound queue being capable of storing a plurality of inbound. queues in a plurality of entries in the outbound queue, each of the plurality of inbound queues storing a plurality of packets that are to be separately transmitted; and

a queue transferring module adapted for transferring the inbound queue storing a plurality of packets to a single entry in the outbound queue such that a reference to the inbound queue is stored in the entry in the outbound queue.

29. (Original) The outbound controller as recited in claim 28, wherein the module adapted for receiving the notification includes a module adapted for receiving the notification from the CPU.

30. (Original) The outbound controller as recited in claim 28, further including:

a module adapted for transmitting packets stored in the outbound queue.

31. (Original) The outbound controller as recited in claim 30, wherein the module adapted for transmitting packets includes:

a module adapted for selectively discarding packets stored in the outbound queue.

32. (Original) The outbound controller as recited in claim 30, wherein the module adapted for transmitting packets stored in the outbound queue includes:

a module adapted for obtaining a next one of the plurality of inbound queues stored in the outbound queue;

a packet transmission module adapted for transmitting selected packets stored in the next one of the plurality of inbound queues; and

a memory releasing module adapted for releasing memory associated with the next one of the plurality of inbound queues.

33. (Original) The outbound controller as recited in claim 32, wherein the memory releasing module includes:  
a module adapted for storing the released memory in a free pool of available packet buffers.

34. (Original) The outbound controller as recited in claim 32, wherein the released memory forms a new inbound queue to be used by an inbound controller.

35. (Original) The outbound controller as recited in claim 32, wherein the released memory, forms a queue to be used by the outbound controller during bi-directional operation.

36. (Previously Amended) The outbound controller as recited in claim 28, wherein the queue transferring module is adapted for transferring the inbound queue to a single entry in the outbound queue according to a priority of the inbound queue.

37. (Previously Amended) A router having a plurality of inbound ports and a plurality of outbound ports, a memory, and a CPU, comprising:

an inbound controller coupled to one of the plurality of inbound ports, the inbound controller being adapted for receiving an inbound packet;

wherein the memory has stored therein:

a plurality of inbound queues for the one of the plurality of inbound ports, each one of the plurality of inbound queues being capable of storing a plurality of packets that are to be separately transmitted;

a plurality of outbound queues, each one of the plurality of outbound queues corresponding to one of the plurality of outbound ports and being capable of storing a plurality of inbound queues such that a reference to each of the plurality of inbound queues is stored in a different one of a plurality of entries in the one of the plurality of outbound queues; and

a classifier coupled to the inbound controller, the classifier being adapted for classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria, the selected one of the plurality of inbound queues being associated with one of the plurality of outbound queues;

wherein the inbound controller is adapted for storing the inbound packet in the selected one of the plurality of inbound queues.

38. (Previously Amended) The router as recited in claim 37, further including:

an outbound controller coupled to the inbound controller;

wherein the inbound controller selects one of the plurality of inbound queues to be transferred to the outbound controller;

wherein the outbound controller is adapted for storing a reference to the selected one of the plurality of inbound queues in an entry in one of the plurality of outbound queues associated with the packet sorting criteria and transmitting packets stored in the one of the plurality of outbound queues.

39. (Original) The router as recited in claim 37, wherein the inbound controller further includes:

a memory obtaining module coupled to the classifier, the memory obtaining module being adapted for obtaining memory for an inbound packet to permit the inbound packet to be stored in the selected one of the plurality of inbound queues in which the inbound packet is classified.

40. (Original) The router as recited in claim 38, wherein the outbound controller further includes:

a memory releasing module adapted for releasing selected packet buffers associated with packets stored in the one of the plurality of outbound queues.

41. (Original) The router as recited in claim 40, wherein the memory further includes a free pool of available packet buffers and the memory releasing module is adapted for releasing the selected packet buffers into the free pool.

42. (Original) The router as recited in claim 38, wherein the outbound controller further includes:

a memory releasing module adapted for providing a new inbound queue to the inbound controller to replace the selected one of the plurality of inbound queues.

43. (Previously Amended) An encryption system, comprising:

an inbound controller adapted for receiving an inbound packet;

a classifier coupled to the inbound controller and adapted for classifying and storing the inbound packet in one of a plurality of inbound queues;

an outbound controller adapted for receiving the one of the plurality of inbound queues, the one of the plurality of inbound queues storing a plurality of packets to be separately transmitted; and

an encryption box coupled to the outbound controller, the encryption box being adapted for encrypting the one of the plurality of inbound queues to provide an encrypted inbound queue to the outbound controller for transmission, wherein the outbound controller includes an outbound classifier adapted for classifying the encrypted inbound queue in one of a plurality of outbound queues associated with a plurality of outbound ports, the outbound controller being adapted for storing a reference to the encrypted inbound queue in a single entry in the one of the plurality of outbound queues, and transmitting data stored in the one of the plurality of outbound queues.

44. (Cancelled)

45. (Previously Amended) The method as recited in claim 2, further comprising:

when the interrupt is asserted, transferring the one of the plurality of inbound queues to an entry in the outbound queue or an outbound controller associated with the outbound queue.

46. (Previously Amended) The method as recited in claim 45, wherein transferring the one of the plurality of inbound queues to an entry in the outbound queue or an outbound controller associated with the outbound queue is performed by the CPU.

47. (Previously Amended) The method as recited in claim 45, wherein transferring the one of the plurality of inbound queues to an entry in the outbound queue or an outbound controller associated with the outbound queue comprises:

transferring a reference to the one of the plurality of inbound queues to an entry in an outbound queue corresponding to a priority associated with the one of the plurality of inbound queues.

48. (Previously Amended) The method as recited in claim 45, wherein transferring the one of the plurality of inbound queues to the outbound queue or an outbound controller associated with the outbound queue comprises:

transferring a pointer to the one of the plurality of inbound queues to an entry in an outbound queue associated with the one of the plurality of inbound queues.

49. (Currently Amended) The method as recited in claim 43, wherein the one of a plurality of inbound queues stores therein a plurality of packets, and wherein the encryption box does not encrypt each of the plurality of packets.

50. (Currently Amended) The method as recited in claim 43, wherein the encryption box is adapted for encrypting the one of a plurality of inbound queues as an entity such that a single encryption step is performed.

51. (Previously Amended) The method as recited in claim 10, wherein transferring the inbound queue to a single entry in the outbound queue is performed by the CPU in response to an interrupt.

52. (Previously Amended) The method as recited in claim 1, wherein the outbound queue comprises a plurality of entries, each of the plurality of entries storing or identifying one of the multiplicity of inbound queues.

53. (Previously Amended) A computer-readable medium storing thereon computer-readable instructions for forwarding a packet in a router, the router having a plurality of inbound ports and a plurality of outbound ports, a memory, and a CPU, the method comprising:

- instructions for providing a plurality of inbound queues for one of the plurality of inbound ports;

- instructions for providing a plurality of outbound queues, each one of the plurality of outbound queues corresponding to one of the plurality of outbound ports and being capable of storing a plurality of inbound queues such that a reference to each of the plurality of inbound queues is simultaneously stored in a different one of a plurality of entries in the one of the plurality of outbound queues;

- instructions for receiving an inbound packet at the one of the plurality of inbound ports;

- instructions for classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria;

- instructions for storing the inbound packet in the selected one of the plurality of inbound queues;

- instructions for repeating the steps of receiving, classifying, and storing until an interrupt is asserted; and

- instructions for transferring one of the plurality of inbound queues to one of the plurality of outbound queues corresponding to the packet sorting criteria when the interrupt is asserted such that a pointer to the transferred one of the plurality of inbound queues is stored in one of a plurality of entries in the one of the plurality of outbound queues, the one of the plurality of inbound queues storing a plurality of packets that are to be separately transmitted.

54. (Previously Amended) An apparatus for forwarding a packet in a router, the router having a plurality of inbound ports and a plurality of outbound ports, a memory, and a CPU, the method comprising:

- means for providing a plurality of inbound queues for one of the plurality of inbound ports;

- means for providing a plurality of outbound queues, each one of the plurality of outbound queues corresponding to one of the plurality of outbound ports and being capable of storing a plurality of inbound queues such that a reference to each of the plurality of inbound queues is stored in a different one of a plurality of entries in the one of the plurality of outbound queues;

- means for receiving an inbound packet at the one of the plurality of inbound ports;

- means for classifying the inbound packet in a selected one of the plurality of inbound queues according to packet sorting criteria;

- means for storing the inbound packet in the selected one of the plurality of inbound queues;

- means for repeating the steps of receiving, classifying, and storing until an interrupt is asserted; and

- means for transferring one of the plurality of inbound queues to one of the plurality of outbound queues corresponding to the packet sorting criteria when the interrupt is asserted such that a reference to the transferred one of the plurality of inbound queues is stored in a single one of a plurality of entries in the one of the plurality of outbound queues, the one of the plurality of inbound queues storing a plurality of packets that are to be separately transmitted.

3. Any comments Applicants considers necessary must be submitted no later than the payment of the Issue Fee and to avoid processing delays, should preferable accompany the Issue Fees. Such submission should be clearly labeled " Comments on Statement of Reasons for Allowance". In event of any post-allowance papers (e.g. IDS, 312 amendment, petition, etc.), Applicant is exhorted to mail papers to the Production Control branch in Publications or faxed to post-allowance papers correspondence branch at (703) 308-5864 to expedite issuing process or call PUB's Customer Service if any questions at (703) 305-8497.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Prieto, B. whose telephone number is (571) 272-3902. The Examiner can normally be reached on Monday-Friday from 6:00 to 3:30 p.m. If attempts to reach the examiner by telephone are unsuccessful, the Examiner's Supervisor, Andrew T. Caldwell can be reached at (571) 272-3868. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3800/4700.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system, status information for published application may be obtained from either Private or Public PAIR, for unpublished application Private PAIR only (see <http://pair-direct.uspto.gov> or the Electronic Business Center at 866-217-9197 (toll-free).

Any response to this action should be mailed to:  
Commissioner of Patents and Trademarks  
P.O. Box 1450  
Alexandria, VA 22313-1450

Hand carried or delivered to:  
Customer Service Window located at the Randolph Bldg.  
401 Dulany St.  
Alexandria, VA 22314

Faxed to the Central Fax Office:  
(703) 872-9306 (old No. in service until Sept. 15, 2005),  
(571) 273-8300 (New Central Fax No.)

Or Telephone:

(703) 306-5631 for TC 2100 Customer Service Office.

  
**BEATRIZ PRIETO**  
PRIMARY EXAMINER